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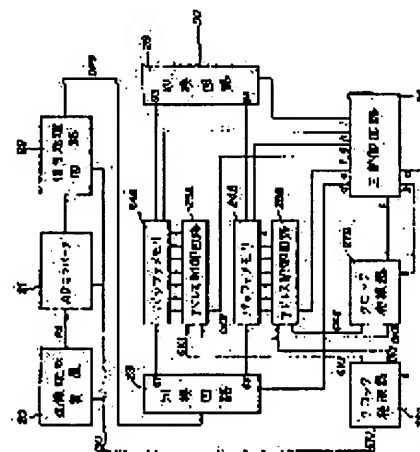YAMAMOTO FUJIO

(54) OPTICAL READER

(57) Abstract:

PURPOSE: To attain magnificating or reducing processing of a picture with simple processing by reading picture data written at a prescribed speed at higher or lower speed to use the data as reduced or magnified data.

CONSTITUTION: A picture signal PS from a picture reader 20 is fed to an A/D converter 21 and converted into a digital signal by using a clock CK1. The digital signal is subjected to processing such as dither processing by a signal processing circuit 22 and written alternately on buffer memories 24A, 24B by way of a switching circuit 23. The write on the memories 24A, 24B uses command signals g, h of a main control circuit 28 with a clock CK1. While one of the buffer memories 24A, 24B is under writing, the other is in read processing and a switch circuit 26 is controlled by using a command signal from the main control circuit 28 and then an output is given. The read clock is a CK2 and the picture is magnified or reduced by changing properly the frequency of the clocks CK2 and CK1.

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